

O I P E SC103
DEC 2 2 2003
Information Disclosure Statement Transmittal

Patent

Docket No.: TRAN-P185

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

Date of Deposit:	12/16/03	Name of Person Making the Deposit:	KATHERINE RINALDI	Signature of the Person Making the Deposit:	<i>Katherine Rinaldi</i>
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Richard Johnson, Andrew Morgan, H. Peter Anvin and Linus Torvalds

Serial No.: 10/719,879 Group Art Unit:

Filed: 11/20/03 Examiner:

Title: ARCHITECTURE, SYSTEM AND METHOD FOR OPERATING ON ENCRYPTED AND/OR HIDDEN INFORMATION

Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450
Sir:

Information Disclosure Statement Transmittal

Transmitted herewith is the following:

..... Formal drawings, totaling sheets.

..... Informal drawings, totaling sheets.

Certification for PTO Consideration

- Information Disclosure statement (3 sheets)
 Information Disclosure statement and late filing fee
 Form 1449
 Petition for Extension of Time
 Other: REFERENCES

Fee Calculation (for other than a small entity)

Fee Items				Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)				\$.00	
Information Disclosure Statement, late filing				\$180.00	
Other:					
Total Fees					\$0.00

PAYMENT OF FEES

1. The full fee due in connection with this communication is provided as follows:
 The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085. A duplicate copy of this authorization is enclosed.
- [] A check in the amount of \$ _____
- [] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060



Date: 12/16/2003

Respectfully submitted,

By:

A handwritten signature in black ink, appearing to read "Anthony C. Murabito".

Anthony C. Murabito
Reg. No. 35,295



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: TRAN-P185

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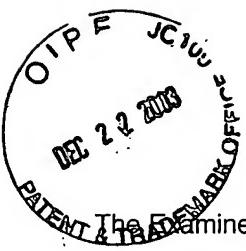
Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

Pat. No.	Pat. Title	Grant Date
6,507,904	EXECUTING ISOLATED MODE INSTRUCTIONS IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	01/14/03
6,463,535	SYSTEM AND METHOD FOR VERIFYING THE INTEGRITY AND AUTHORIZATION OF SOFTWARE BEFORE EXECUTION IN A LOCAL PLATFORM	10/08/02
6,415,379	METHOD AND APPARATUS FOR MAINTAINING CONTEXT WHILE EXECUTING TRANSLATED INSTRUCTIONS	07/02/02
6,408,376	METHOD AND APPARATUS FOR INSTRUCTION SET ARCHITECTURE TO PERFORM PRIMARY AND SHADOW DIGITAL SIGNAL PROCESSING SUB-INSTRUCTIONS SIMULTANEOUSLY	06/18/02
6,401,208	METHOD FOR BIOS AUTHENTICATION PRIOR TO BIOS EXECUTION	06/14/02
6,327,660	METHOD FOR SECURING COMMUNICATIONS IN A PRE-BOOT ENVIRONMENT	12/04/01
6,363,486	METHOD OF CONTROLLING USAGE OF SOFTWARE COMPONENTS	03/26/02
6,249,872	METHOD AND APPARATUS FOR INCREASING SECURITY AGAINST UNAUTHORIZED WRITE ACCESS TO A PROTECTED MEMORY	06/19/01
6,199,152	TRANSLATED MEMORY PROTECTION APPARATUS FOR AN ADVANCED MICROPROCESSOR	03/06/01
6,175,896	MICROPROCESSOR SYSTEM AND METHOD FOR INCREASING MEMORY BANDWIDTH FOR DATA TRANSFERS BETWEEN A CACHE AND MAIN MEMORY UTILIZING DATA COMPRESSION	01/16/01
6,038,320	COMPUTER SECURITY KEY	03/14/00
6,031,992	COMBINING HARDWARE AND SOFTWARE TO PROVIDE AN IMPROVED MICROPROCESSOR	02/29/00
6,011,908	GATED STORE BUFFER FOR AN ADVANCED MICROPROCESSOR	01/04/00
5,034,980	MICROPROCESSOR FOR PROVIDING COPY PROTECTION	07/23/91



The Examiner's attention is respectfully directed to the following U.S. Patents:

M. Fordahl; "TRANSMETA UNVEILS LONG-AWAITED NEXT-GENERATION PROCESSOR"; 10/14/03;
http://biz.yahoo.com/ap/031014/na_fin_com_us_transmeta_new_chip_2.html; 2 pgs.

Microsoft; "NEXT-GENERATION SECURE COMPUTING BASE, THE ROAD TO SECURITY"; 09/08/03; <http://www.microsoft.com/resources/ngscb/default.mspx>; 2 pgs.

Microsoft; "THE NEXT-GENERATION SECURE COMPUTING BASE: AN OVERVIEW"; April 2003; http://www.microsoft.com/resources/NGSCB_overview.mspx; 2 pgs.

Microsoft; "NEXT-GENERATION SECURE COMPUTING BASE, PRODUCT INFORMATION"; 09/08/03; <http://www.microsoft.com/resources/ngscb/productInfo.mspx>; 2 pgs.

Microsoft; "THE NEXT-GENERATION SECURE COMPUTING BASE: FOUR KEY FEATURES"; June 2003; http://www.microsoft.com/resources/ngscb/four_features.mspx; 3 pgs.

Microsoft; "NEXT-GENERATION SECURE COMPUTING BASE- TECHNICAL FAQ"; July 2003; <http://www.microsoft.com/technet/security/news/NGSCB.asp?frame=true>; 9 pgs.

Microsoft; "Press Pass Information for Journalists; Q & A: MICROSOFT SEEKS INDUSTRY-WIDE COLLABORATION FOR "PALLADIUM" INITIATIVE"; 01/25/03;
<http://www.microsoft.com/presspass/features/2002/jul02/07-01palladium.asp>; 4 pgs.

Microsoft; "Press Pass Information for Journalists; THE JOURNEY TO TRUSTWORTHY COMPUTING: MICROSOFT EXEC'S REPORT FIRST-YEAR PROGRESS"; 01/15/03;
<http://www.microsoft.com/presspass/features/2003/jan03/01-15twcanniversary.asp>; 7 pgs.

Microsoft; "Press Pass Information for Journalists; Q & A: DELIVERING ON SECURE COMPUTING"; 04/14/03; <http://www.microsoft.com/presspass/features/2003/apr03/04-14rsanash.asp>; 6 pgs.

Microsoft; "Press Pass Information for Journalists; AT WINHEC, MICROSOFT DISCUSSES DETAILS OF NEXT GENERATION SECURE COMPUTING BASE"; 05/07/03;
<http://www.microsoft.com/presspass/features/2003/may03/05-07NGSCB.asp>; 4 pgs.

Transmeta; "CRUSOE-THE TECHNOLOGY-THE ARCHITECTURE-CODE MORPHING SOFTWARE"; 03/14/03;
http://www.transmeta.com/technology/architecture/code_morphing.html; 3pgs.

Transmeta; "CRUSOE-THE TECHNOLOGY-THE ARCHITECTURE-LONGRUN POWER MANAGEMENT TECHNOLOGY"; 03/14/03;
<http://www.transmeta.com/technology/architecture/longrun.html>; 3pgs.

Microsoft; "HARDWARE PLATFORM FOR THE NEXT-GENERATION SECURE COMPUTING BASE"; 2003 Microsoft Corp.; 10 pgs.

Microsoft; "NGSCB: TRUSTED COMPUTING BASE AND SOFTWARE AUTHENTICATION"; 2003 Microsoft Corp. 16 pgs.



Microsoft; "SECURITY MODEL FOR THE NEXT-GENERATION SECURE COMPUTING BASE", 2003 Microsoft Corp. 13 pgs.

A. Klaiber; "THE TECHNOLOGY BEHIND CRUSOE PROCESSORS LOW POWER X86-COMPATIBLE PROCESSORS IMPLEMENTED WITH CODE MORPHING SOFTWARE"; January 2000; 18 pgs.

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Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
A	6,507,904	01/14/03	Ellison	712	229	03/31/00	
B	5,463,535	10/08/02	Drews	713	176	10/05/98	
C	6,415,379	07/02/02	Keppel et al.	712	209	10/13/99	
D	6,408,376	06/18/02	Ganapathy et al.	712	36	08/30/00	
E	6,401,208	06/04/02	Davis et al.	713	193	07/17/98	
F	6,327,660	12/04/01	Patel	713	193	09/18/98	
G	6,363,486	03/26/02	Knapton, III	713	200	06/05/98	
H	6,249,782	06/19/01	Wildgrube et al.	713	200	01/05/98	
I	6,199,152	03/06/01	Kelly et al.	711	207	08/22/96	
J	6,175,896	01/16/01	Bui	711	118	10/06/97	
K	6,038,320	03/14/00	Miller	380	44	10/11/96	
L	6,031,992	02/29/00	Cmeliik et al.	395	705	07/05/96	
M	6,011,908	01/04/00	Wing et al.	395	182.17	12/23/96	
N	5,034,980	07/23/91	Kubota	380	4	06/21/90	

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
O								



Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	P	M. Fordahl; "TRANSMETA UNVEILS LONG-AWAITED NEXT-GENERATION PROCESSOR"; 10/14/03; http://biz.yahoo.com/ap/031014/na_fin_com_us_transmeta_new_chip_2.html ; 2 pgs.
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	R	Microsoft; "THE NEXT-GENERATION SECURE COMPUTING BASE: AN OVERVIEW"; April 2003; http://www.microsoft.com/resources/NGSCB_overview.mspx ; 2 pgs.
	S	Microsoft; "NEXT-GENERATION SECURE COMPUTING BASE, PRODUCT INFORMATION"; 09/08/03; http://www.microsoft.com/resources/ngscb/productInfo.mspx ; 2 pgs.
	T	Microsoft; "THE NEXT-GENERATION SECURE COMPUTING BASE: FOUR KEY FEATURES"; June 2003; http://www.microsoft.com/resources/ngscb/four_features.mspx ; 3 pgs.
	U	Microsoft; "NEXT-GENERATION SECURE COMPUTING BASE- TECHNICAL FAQ"; July 2003; http://www.microsoft.com/technet/security/news/NGSCB.asp?frame=true ; 9 pgs.
	V	Microsoft; "Press Pass Information for Journalists; Q & A: MICROSOFT SEEKS INDUSTRY-WIDE COLLABORATION FOR "PALLADIUM" INITIATIVE"; 01/25/03; http://www.microsoft.com/presspass/features/2002/jul02/07-01palladium.asp ; 4 pgs.
	W	Microsoft; "Press Pass Information for Journalists; THE JOURNEY TO TRUSTWORTHY COMPUTING: MICROSOFT EXECs REPORT FIRST-YEAR PROGRESS"; 01/15/03; http://www.microsoft.com/presspass/features/2003/jan03/01-15twcanniversary.asp ; 7 pgs.
	X	Microsoft; "Press Pass Information for Journalists; Q & A: DELIVERING ON SECURE COMPUTING"; 04/14/03; http://www.microsoft.com/presspass/features/2003/apr03/04-14rsanash.asp ; 6 pgs.
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	AA	Transmeta; "CRUSOE-THE TECHNOLOGY-THE ARCHITECTURE-LONGRUN POWER MANAGEMENT TECHNOLOGY"; 03/14/03; http://www.transmeta.com/technology/architecture/longrun.html ; 3pgs.
	BB	Microsoft; "HARDWARE PLATFORM FOR THE NEXT-GENERATION SECURE COMPUTING BASE"; 2003 Microsoft Corp.; 10 pgs.
	CC	Microsoft; "NGSCB: TRUSTED COMPUTING BASE AND SOFTWARE AUTHENTICATION"; 2003 Microsoft Corp. 16 pgs.
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	EE	A. Klaiber; "THE TECHNOLOGY BEHIND CRUSOE PROCESSORS LOW POWER X86-COMPATIBLE PROCESSORS IMPLEMENTED WITH CODE MORPHING SOFTWARE"; January 2000; 18 pgs.
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.